

FIG. 1

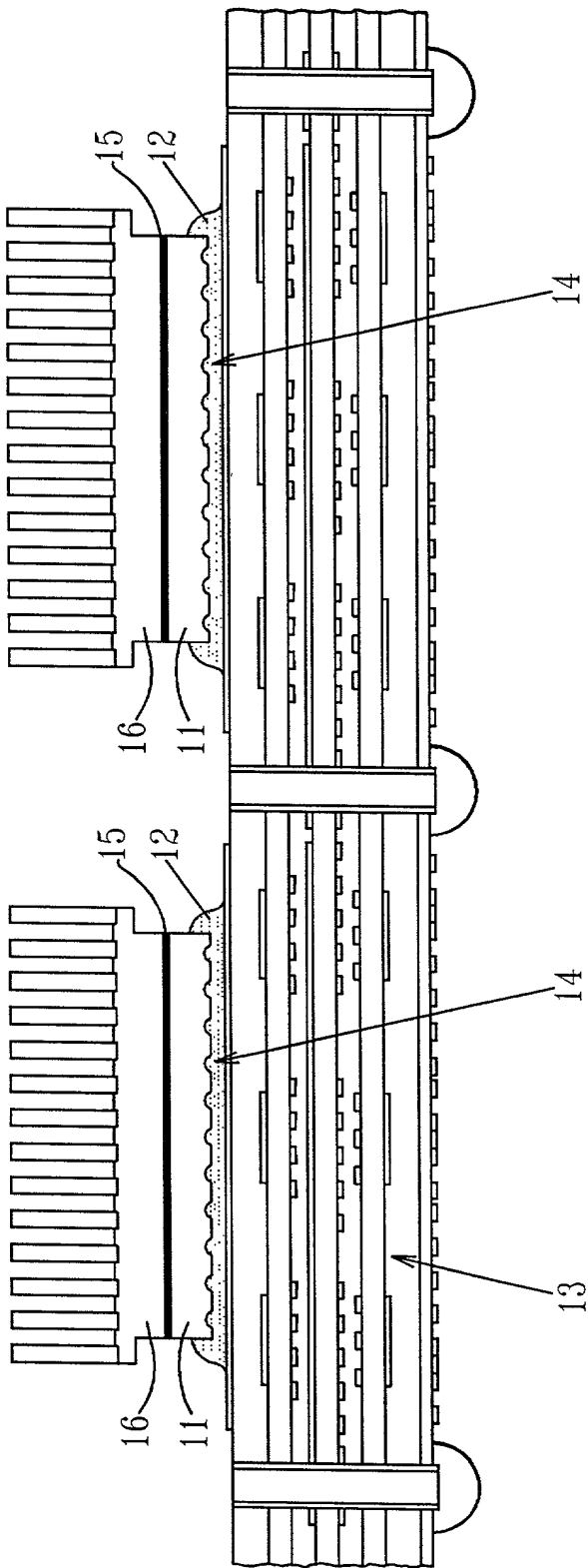


FIG. 2

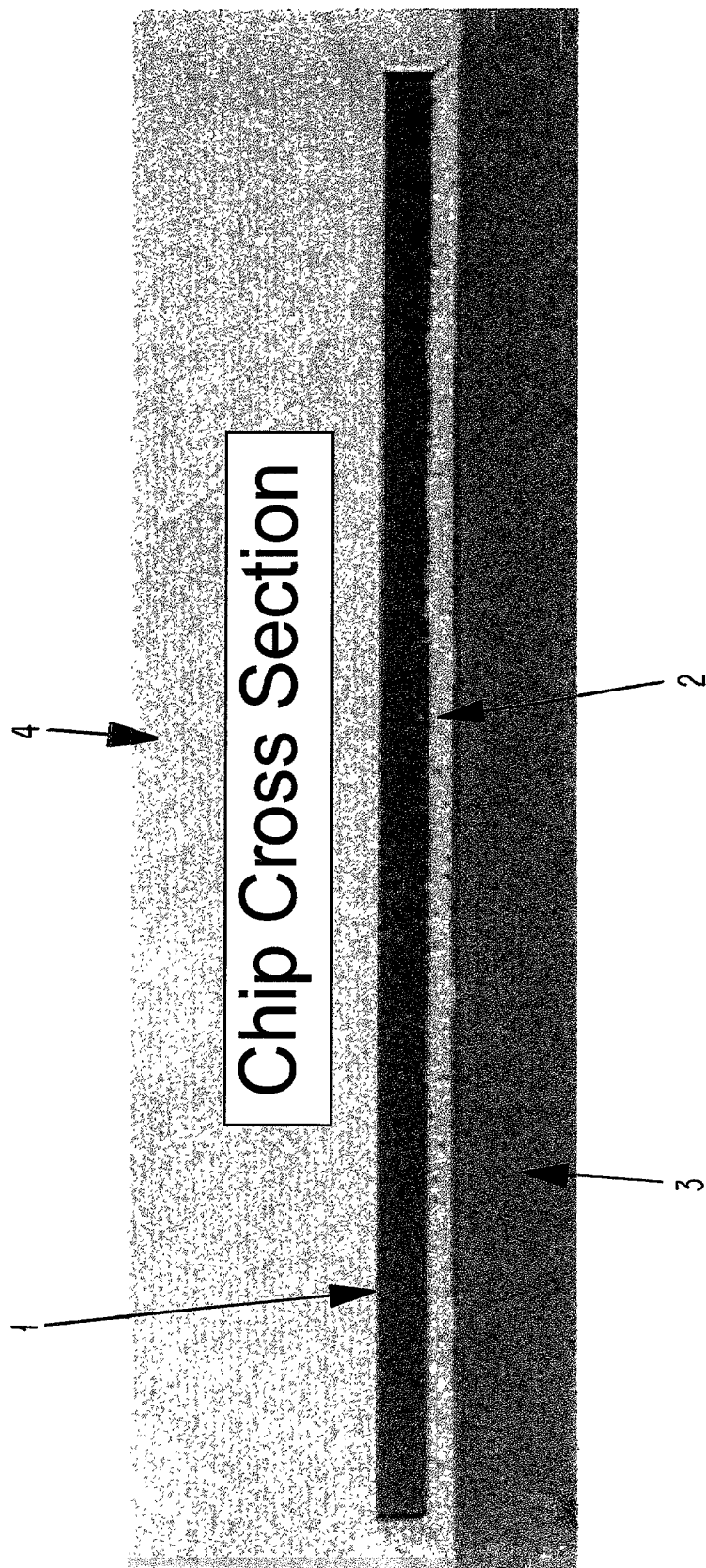


FIG. 3

FIG. 3 is a cross-sectional view of the chip assembly.

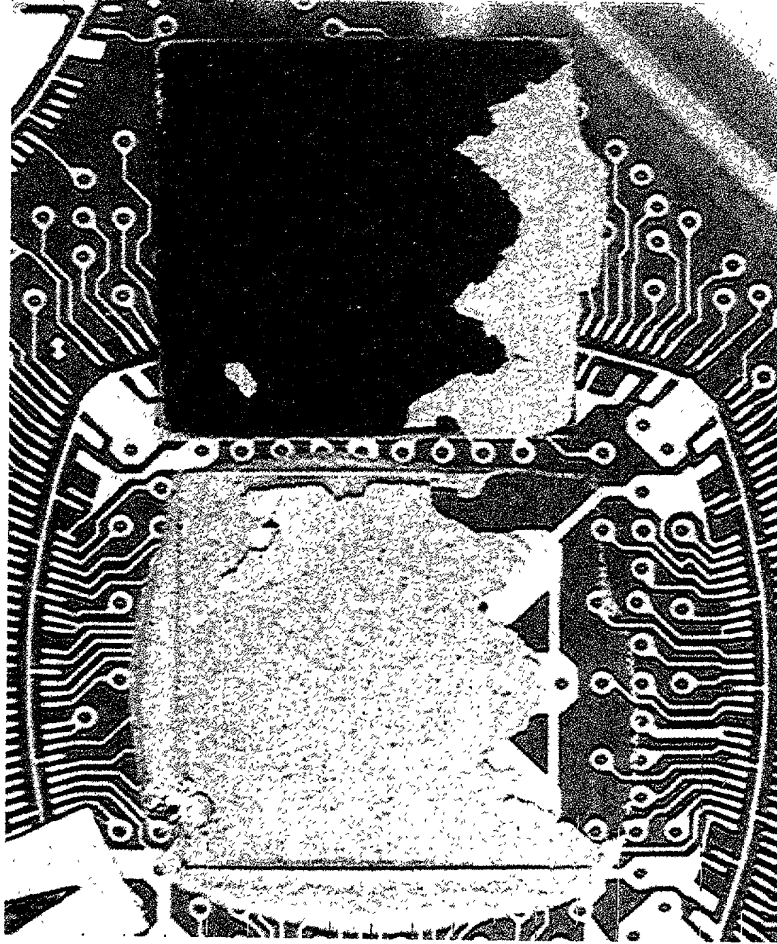


FIG. 4

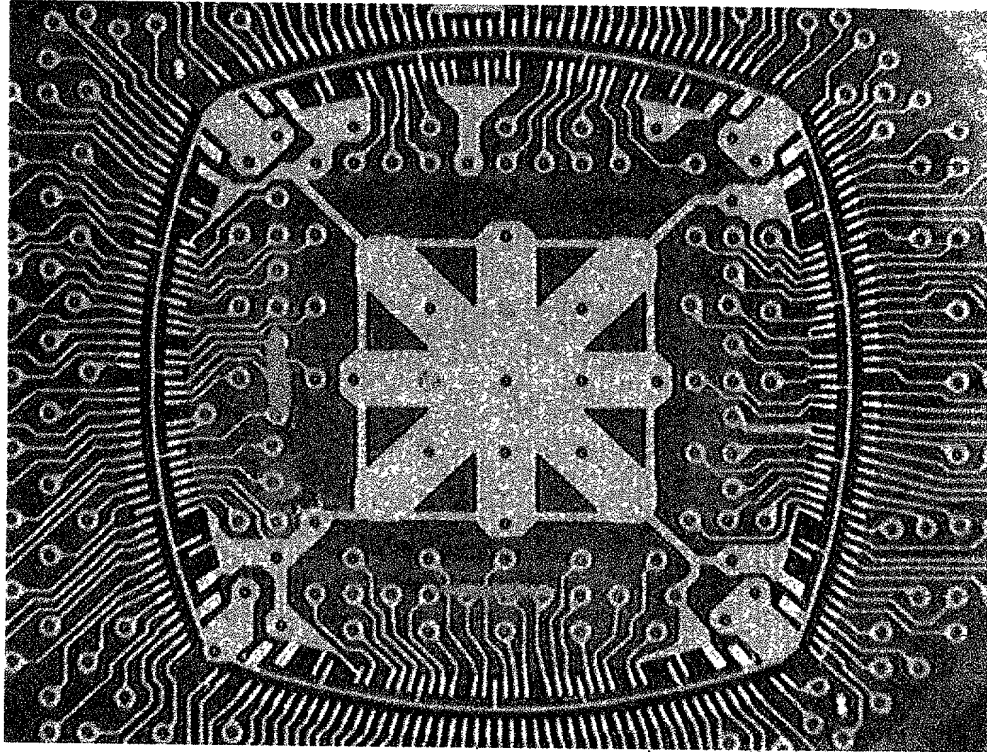


FIG. 5

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